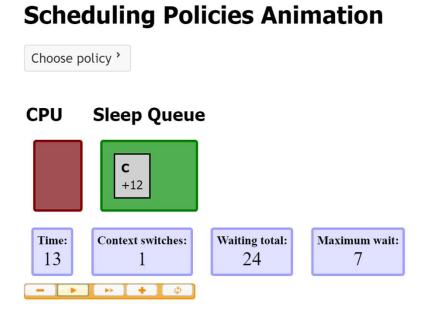
## Dr. Onur Tolga Sehitoğlu's Scheduler Simulator

http://sehitoglu.web.tr/scheddemo/#



# P Pattern Pri Arrive A 10 3 3 1 2 2 B 5 2 1 2 2 0 2 C 2 15 10 1 1 1

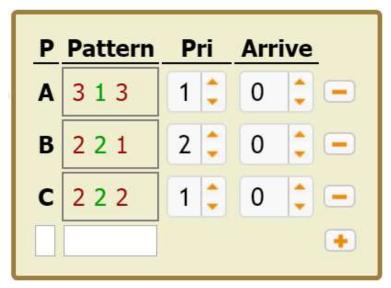
#### **First Come First Served**



#### History

t	0	1	2	3	4	5	6	7	8	9	10	11	12	13
A			W	W	W	W	W	W	W	W	W	W	W	W
в	R	R	R	R	R	s	S	R						
С		W	W	W	W	W	W	W	R	R	S	S	S	S





3

R

2

S

0

R

t

C

1

R

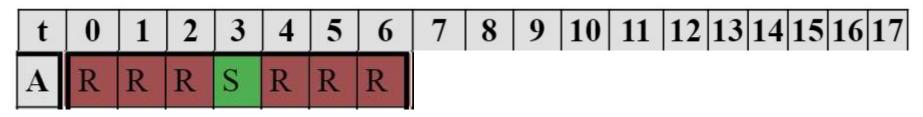
S

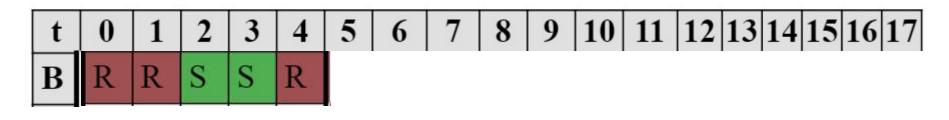
5

6

4

R





7

8

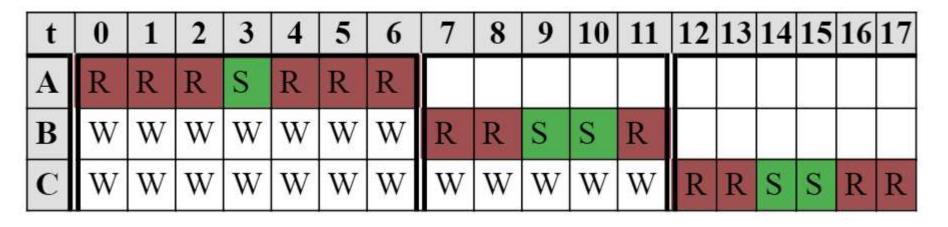
9

10 11 12 13 14 15 16 17

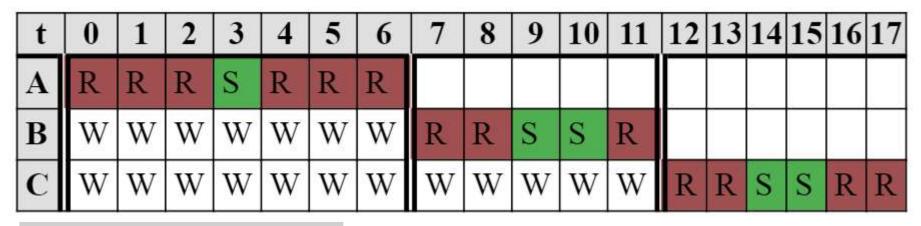
#### FCFS

- Batch processing.
- Each process runs to its completion, before the next one in the queue.

Ρ	Pattern	Pri	Arr	ive	
A	313	1	0	•	
в	221	2 🗘	0	•	-
С	222	1 🛟	0	•	
					•



#### FCFS



#### **Events**

(0) process A has arrived (0) process B has arrived (0) process C has arrived (0) process A is now running (3) process A is going to sleep for 1 ticks (3) CPU is idle (4) process A woke up (4) process A is now running (7) process A terminated (7) process B is now running (9) process B is going to sleep for 2 ticks (9) CPU is idle (10) CPU is idle (11) process B woke up (11) process B is now running (12) process B terminated (12) process C is now running (14) process C is going to sleep for 2 ticks (14) CPU is idle (15) CPU is idle (16) process C woke up (16) process C is now running (18) process ( terminated

#### FCFS – switch on sleep

When the current process goes to sleep, it switches to the next one on the ready queue.

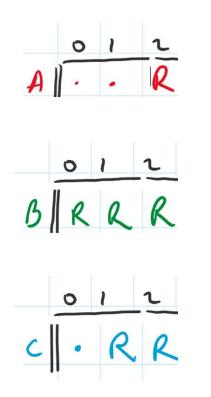
P Patter	n Pri	Arriv	<u>e</u>
<b>A</b> 313	1 🗘	0	
<b>B</b> 2 2 1	2 🌲	0	
<b>C</b> 2 2 2	1 🗘	0	
			•

t	0	1	2	3	4	5	6	7	8	9	10	11	12	13
Α	R	R	R	S	W	W	W	R	R	R				
В	W	W	W	R	R	S	S	W	W	W	R		. 4 T	
C	W	W	W	W	W	R	R	S	S	W	W	R	R	

#### **Process patterns**

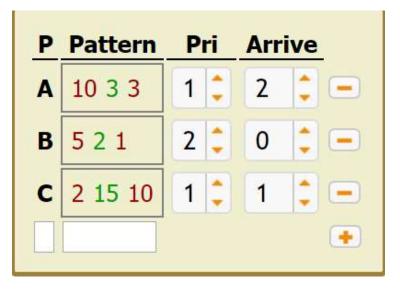
- Arrival times indicate the insertion of that process into the ready queue.
- The execution of each of the processes, if they were the only ones in the system.

<u>P</u>	Pattern	Pri	Arri	ve
A	10 3 3	1 🛟	2	: -
В	521	2 🌲	0	•
С	2 15 10	1	1	: -
				•



### Round Robin (RR) - 3

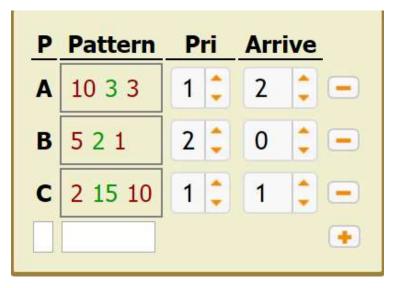
 Maximum CPU allocation before the timer interrupt preempts the process.



t	0		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33
Α				W	W	W	R	R	R	W	W	R	R	R	W	R	R	R	R	S	S	S	W	W	R	R	R								
B	F	2 I	R	R	W	W	W	W	W	R	R	S	S	W	R																				
С		V	W	W	R	R	S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	R	R	R	W	W	W	R	R	R	R	R	R	R	

## Round Robin (RR) - 5

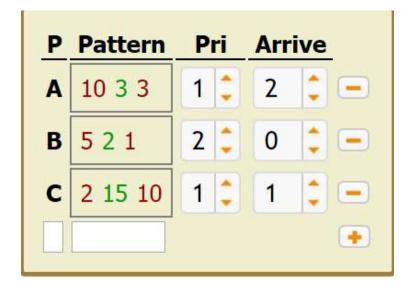
 Maximum CPU allocation before the timer interrupt preempts the process.



t	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
A		<b>.</b> •€	W	W	W	W	W	R	R	R	R	R	W	R	R	R	R	R	S	S	S	R	R	R											
В	R	R	R	R	R	S	S	W	W	W	W	W	R																						
C		W	W	W	W	R	R	S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	W	W	R	R	R	R	R	R	R	R	R	R	

### **Shortest Job First (SJF)**

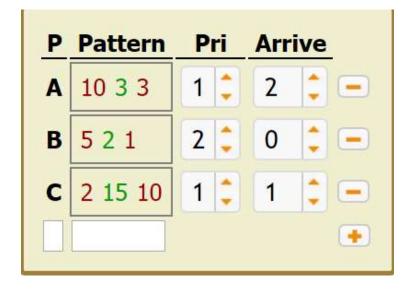
- Assume that the process execution pattern is known by the scheduler
  - Note that, this is not possible in real life. The scheduler can only gather statistics from prior execution behavior of the process and uses that.



t	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
A			W	W	W	W	W	W	R	R	R	R	R	R	R	R	R	R	S	S	S	R	R	R									÷		
B	R	R	R	R	R	S	S	R				2																							
С		W	W	W	W	R	R	S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	W	W	R	R	R	R	R	R	R	R	R	R	

## Shortest Remaining Time First (SRTF)

- Assume that the process execution pattern is known by the scheduler
  - Note that, this is not possible in real life. The scheduler can only gather statistics from prior execution behavior of the process and uses that.



t	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
Α	•		W	W	W	W	W	R	R	W	R	R	R	R	R	R	R	R	S	S	S	R	R	R								
B	R	W	W	R	R	R	R	S	S	R																						
C		R	R	S	S	S	S	S	S	S	S	S	S	S	S	S	S	S	R	R	R	W	W	W	R	R	R	R	R	R	R	